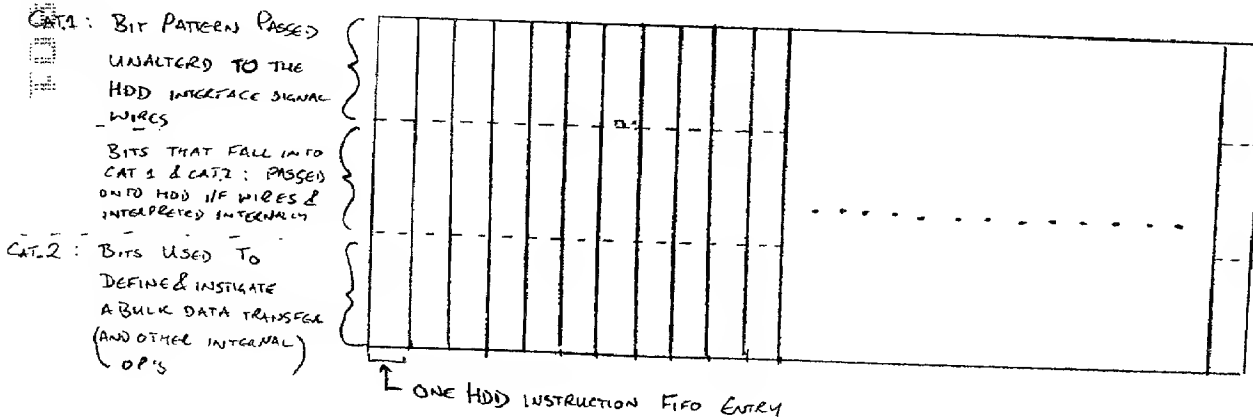


CPU receives data and controls the data which is coming in and then determines which is to be stored and then controls the generation of the signals for the FIFO

FIG 2: HDD INSTRUCTION FIFO. - provides more time to transfer the real time data



1 2 3 4 5 6 etc.

1 2 3 4 5 6 and then referred to for repeats